

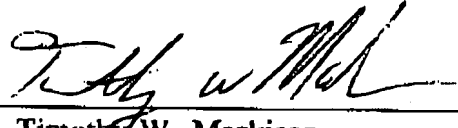
- F1
end
- b) providing the interrupt signal to a processor when the mask override signal is enabled or when the mask signal and the mask override signal are disabled [determining whether a mask signal is enabled]; and
- c) masking the interrupt signal from when the mask signal is enabled and the mask override signal is disabled [when the mask signal is enabled, determining whether a mask override signal is enabled; and
- d) when the mask override signal is enabled, providing the interrupt signal to a processor regardless of whether the mask signal is enabled or disabled].

REMARKS

Pursuant to a telephone conversation between Examiner Ray and Timothy W. Markison, attorney of record, the applicants are submitting this supplemental amendment. With these amendments, the applicants believe that claims 6, 7, and 27 are in condition for allowance.

RESPECTFULLY SUBMITTED,
Bowles et al.

By:


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